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Signature

Oct. 28, 2004

Date

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No.	:	09/922,479	Confirmation No.	7212
Applicant	:	Thomas Zeitler		
Filed	:	August 3, 2001		
Title	:	METHOD AND DEVICE FOR TESTING AN INTEGRATED CIRCUIT, INTEGRATED CIRCUIT TO BE TESTED, AND WAFER WITH A LARGE NUMBER OF INTEGRATED CIRCUITS TO BE TESTED		
Group Art Unit	:	2133		
Examiner	:	Phung M. Chung		
Docket No.	:	J&R-0694		
Customer No.	:	24131		

A M E N D M E N T

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

S i r :

Responsive to the Office action dated July 2, 2004, kindly amend the above-identified application as follows.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims begin on page 3 of this paper.

Remarks/Arguments begin on page 13 of this paper.

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